Workshop about Opportunities for dual-use technologies: Components

## A Micromachined Heterogeneous Integration Platform for THz Systems

Franz Dielacher Infineon Technologies Austria AG Franz.dielacher@infineon.com June 28, 2016 Brussels

Opportunities for dual-use technologies: Components





This project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement No 644039.

This work is supported (also) by the Swiss State Secretariat for Education, Research and Innovation (SERI) under contract number 15.0059. The opinions expressed and arguments employed herein do not necessarily reflect the official views of the Swiss Government.



## **MOTIVATION:**

# THz applications and the need for new ways of building THz systems

## **Opportunities in the THz gap**



**M3**TERA

## mm-Wave and Terahertz (THz) Systems \_4\_M3TERA

Low-cost THz technology in our society Enabled by micromachined heterogeneous integration platform

Wireless-

Security,

Industrialsensing Food control Medical Thickness

300M€ today

€M008

in 2020



**Astrophysics** 

## **Micromachined THz Systems**

### User controlled attenuation input TIL modulation iput TIL modulation iput Trequency FM input

sub-mm-wave system by VDI

#### **Conventional THz system**

- Bulky
- Heavy
- Manually assembled
- Expensive
- Only for scientific instruments

#### Highly-integrated THz system

M**3**TERA

- 1000x smaller
- 1000x lighter
- 10x less power
- reconfigurable
- 100x lower cost
- High volume manufacturable



D110

## Technology Choice versus Application \_4\_M3TERA



## **Technology Decision**



... if there is a business case

\_M**3**TERA

	Infineon	Teledyne	Teledyne	WIN	TSMC
Technology /	SiGe HBT		InP HEMT	pHEMT	CMOS
Feature size	180 nm 120 nm	250 nm	50 nm	100 nm	45 nm
No. met.layers	4 5	5	2	2	9
Complexity	VLSI	MSI - LSI	10 – 50	10 - 50	VLSI
f <sub>T</sub> /f <sub>MAX</sub>	200/250 GHz 250/400 GHz	370/650 GHz	600/600 GHz	120/180 GHz	300/300 GHz
V <sub>BR</sub>	VB <sub>CE0</sub> = 1.8 V VB <sub>CE0</sub> = 1.5 V	VB <sub>CE0</sub> = 4 V	1 V	7 V	1 V

Source: Herbert Zirath, EuMW-2015, WS07 RF-technologies on the move...

## SiGE and CMOS Trends

M**3**TERA

SiGeC





**Source:** SiGeC HBT technology roadmap, IMS2015, Phoenix, AZ, 17-22 May, 2015

Source: Ali Niknejad, Brooklyn5Gsummit April 2015

## **Telecommunication in the THz gap**



**M3**TERA



## **Silicon Power Amplifier Design**



- Power and Efficiency drop with frequency
- SiGe mainly used at frequencies > 10 GHz
- In SiGe > 20 dBm @ 100 GHz is reported but efficiency is an issue

**M3**TERA

## 212 GHz PA Design in 130nm SiGe



#### 212 GHz 4-stage Amplifier in SiGe *fT/fmax* = 250/400 GHz

- Gain: 19.5 dB
- BW: 21 GHz
- 65 mA @ 3.3 V
- Marchand baluns at input and output
- Pads tuned with short circuit stub to resonate the pad capacitance
- T-match at the output (for wide bandwidth)

\_M3TERA

## class A/B PA: PAE



Calculated from HICUM/L2 With all known physical and Parasitic effects (incl. Self-heating)

L M**3**TERA

Node-1: 130nm Node-2: 90nm Node-3: 65nm Node-4: 40nm Node-5: 22nm



## **THZ MICROSYSTEM CONCEPT**





## **Micromachining process**





**M3**TERA

- 1 E-plane split



1 H-plane split



## 3-cavity based diplexer-filter example \_\_\_\_M3TERA



Tunable filters (~15% tuning, 0.4 dB IL @ D-band)



# are pioneering THz micromachining

## 500-750 GHz Submillimeter-Wave MEMS Waveguide Switch

U. Shah<sup>1</sup>, T. Reck<sup>2</sup>, E. Decrossas<sup>2</sup>, C. Jung-Kubiak<sup>2</sup>, H. Frid<sup>1</sup>, G. Chattopadhyay<sup>2</sup>, I. Mehdi<sup>2</sup>, and J. Oberhammer<sup>1</sup>

<sup>1</sup>KTH Royal Institute of Technology, Stockholm, Sweden

<sup>2</sup>Jet Propulsion Laboratory, Pasadena, CA, 91109 USA

### 500-600 GHz Submillimeter-Wave 3.3 bit RF MEMS Phase Shifter Integrated in Micromachined Waveguide

U. Shah<sup>1</sup>, E. Decrossas<sup>2</sup>, C. Jung-Kubiak<sup>2</sup>, T. Reck<sup>2</sup>, G. Chattopadhyay<sup>2</sup>, I. Mehdi<sup>2</sup>, and J. Oberhammer<sup>1</sup> <sup>1</sup>KTH Royal Institute of Technology, Stockholm, Sweden <sup>2</sup>Jet Propulsion Laboratory, Pasadena, CA, 91109 USA

First RF MEMS above 200GHz, First MEMS phase-shifter above 110GHz, ...

MBTERA

## **Resently published example 2**

500-600 GHz Submillimeter-Wave 3.3 bit RF MEMS Phase Shifter Integrated in Micromachined Waveguide





M3TERA



Submillimeter-wave MEMS phase-shifter design using 9 E-plane stubs

Normalized phase-shift of the 500-600GHz phase-shifter Linear phase-shift of 20deg in 10 steps Return-loss < 15dB, insertion-loss < 3dB

- -> Mm-wave and THz frequencies are playing an important role in current and emerging applications like
  - Communications (Microwave backhaul, 5G, ...)
  - Medical, Security, Industrial, Food control, Astronomy, ....
- -> Low-cost THz technology will be enabled by the micromachined heterogeneous integration platform
- -> The major innovation and design issues are
  - Development of the micromachined multisubstrate platforms
  - Integrated circuit design in cost efficient SiGe-BiCMOS technology
  - Mm-wave and THz suited packaging of the ICs
- -> The platform will initiate an important transition in industrial microwave-systems manufacturing and will enable the large-scale commercialization

MBTFRA



# Thank you for your attention



## **Back-up**

29 June, 2016 Opportunities for dual-use technologies: Components

#### 500-750 GHz Submillimeter-Wave MEMS Waveguide Switch



MEMS waveguide switch a.) Schematic cross-section of the switch and b.) Non-blocking and blocking state of the switch

**M3**TERA

#### **Measurement:**

Better 15dB isolation in blocking Better 3dB insertion-loss in non-blocking for 500-750GHz

IEEE IMS-2016 Conference Paper M3TERA partner KTH Royal Institute of Technology and NASA Jet Propulsion Laboratory Pasadena, CA



## M3TERA Grant Agreement No. 644039

"The **M3TERA** project has received funding from the European Union's Horizon 2020 research and innovation programme under grant agreement number 644039."

This work is supported (also) by the Swiss State Secretariat for Education, Research and Innovation (SERI) under contract number 15.0059.

The opinions expressed and arguments employed herein do not necessarily reflect the official views of the Swiss Government.

If you need further information, please contact the coordinator: TECHNIKON Forschungs- und Planungsgesellschaft mbH Burgplatz 3a, 9500 Villach, AUSTRIA Tel: +43 4242 233 55 Fax: +43 4242 233 55 77 E-Mail: coordination@m3tera.eu

The information in this document is provided "as is", and no guarantee or warranty is given that the information is fit for any particular purpose. The user thereof uses the information at its sole risk and liability.